

## ABSTRACT

A method and apparatus for enabling and disabling a pull-up resistor connected to a data line of a universal serial bus is provided for universal serial bus devices that are provided with an external power source. A transistor is interconnected between VBUS of the universal serial bus connection and the input of an integrated circuit such that the voltage on the input of the integrated circuit is proportional to the voltage on VBUS. Thus, when VBUS is not present the pull-up resistor on the data line is disabled and subsequently enabled when VBUS exceeds a predetermined threshold.